

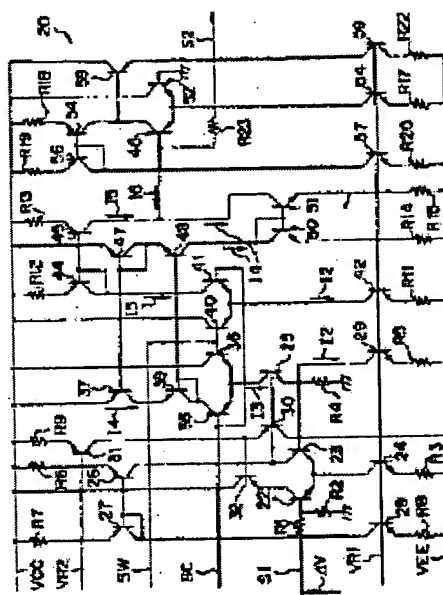
VARIABLE GAIN AMPLIFIER CIRCUIT

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Abstract of JP4227109

PURPOSE: To improve an S/N while keeping a wide dynamic range and to reduce power consumption by applying voltage-current conversion to an input signal, outputting the result to a differential amplifier circuit together with an idling current to control a gain.

CONSTITUTION: A TR29 drives a TR32 corresponding to the collector output of a TR23 in a state where a prescribed current decided by a reference voltage VR2 is supplied. The TR32 feeds back the collector output of the TR23 to the TR23. Assuming the collector current of the TR29 as I2, the collector current I3 of a TR25 is changed corresponding to the signal level of a chrominance signal S3: $I3 = I2 + V/R4$. The signal S1 is voltage-current converted by the TR22-32, and the current I3 is changed following the signal level of the signal S1 centering the current value I2 decided by a power source (29, I5). The collector current of a TR35 is changed corresponding to a shading correction signal SW and control signal SC, and the chrominance signal can be amplified by a desired gain.



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